

● PRINTER RUSH ●
(PTO ASSISTANCE)

Every Check HC

Application : 09/702293

Examiner : Han

GAU : 2665

From: CA

Location: IDC FDC FDC

Date: 9/29/05

Tracking #: _____ Week Date: _____

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DOC CODE	DOC DATE	MISCELLANEOUS
<input type="checkbox"/> 1449	_____	<input type="checkbox"/> Continuing Data
<input type="checkbox"/> IDS	_____	<input type="checkbox"/> Foreign Priority
<input type="checkbox"/> CLM	_____	<input type="checkbox"/> Document Legibility
<input type="checkbox"/> IIFW	_____	<input type="checkbox"/> Fees
<input type="checkbox"/> SRFW	_____	<input type="checkbox"/> Other
<input checked="" type="checkbox"/> DRW	<u>10/30/00</u>	
<input type="checkbox"/> OATH	_____	
<input type="checkbox"/> 312	_____	
<input type="checkbox"/> SPEC	_____	

[RUSH] MESSAGE: Attn: Chief Draftsperson

Figures 5, 8 + 12 have the date stamp interfering with figures.

Please resolve. Thank you

[XRUSH] RESPONSE: _____

(COLLECTED DRAWINGS)

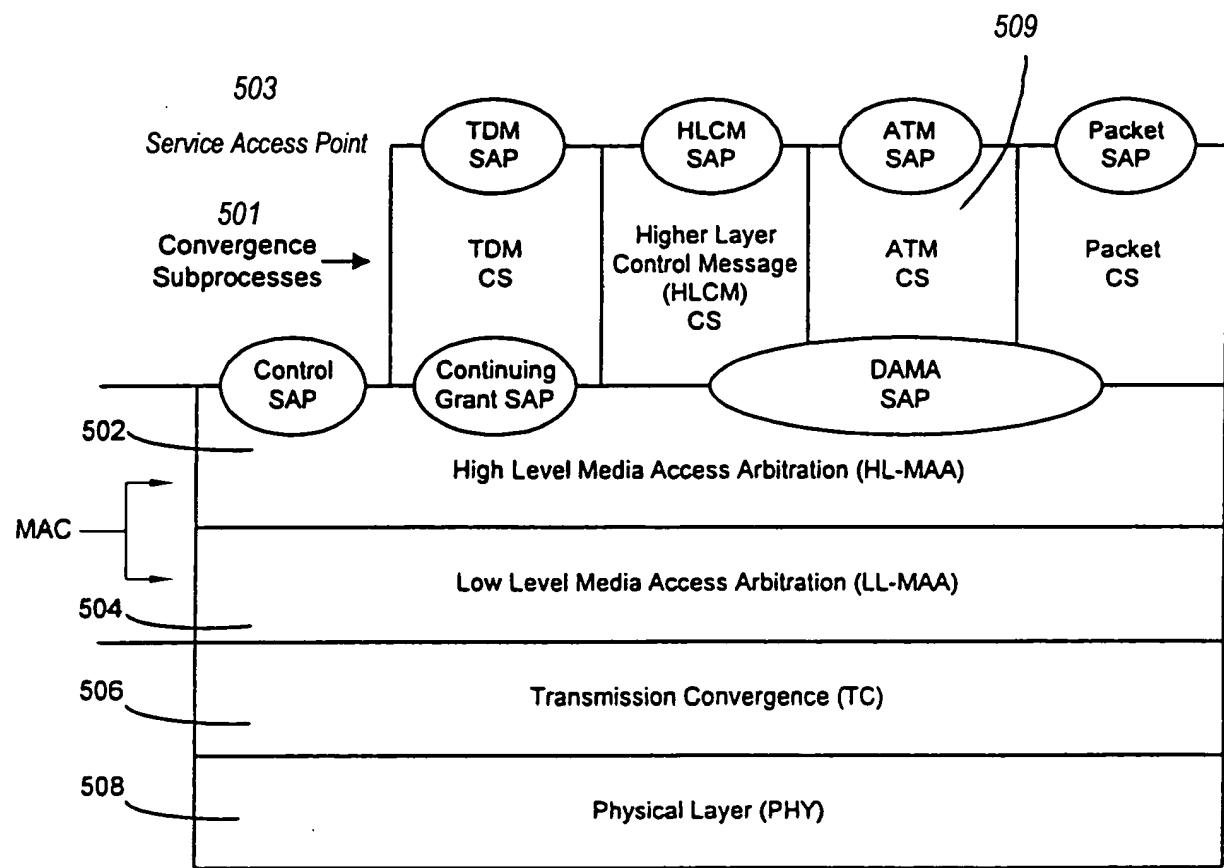
SEE ATTACHMENT

10-13-05

INITIALS: JDC

NOTE: This form will be included as part of the official USPTO record, with the Response document coded as XRUSH.

REV 10/04



Layered Data Transport Architecture

FIG. 5

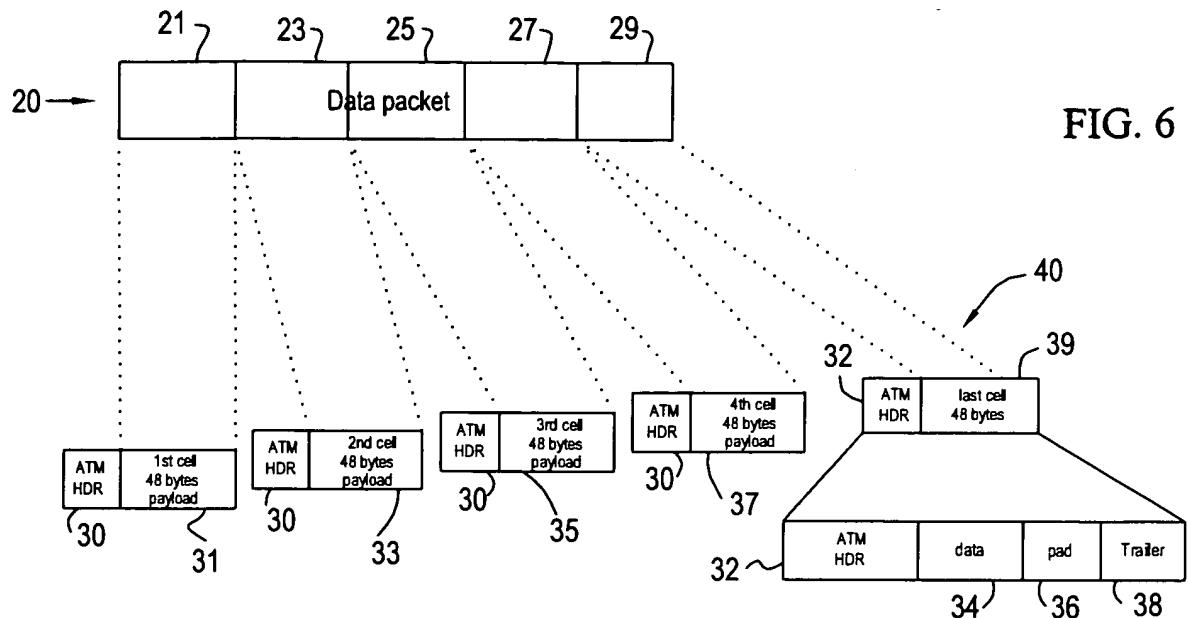


FIG. 6

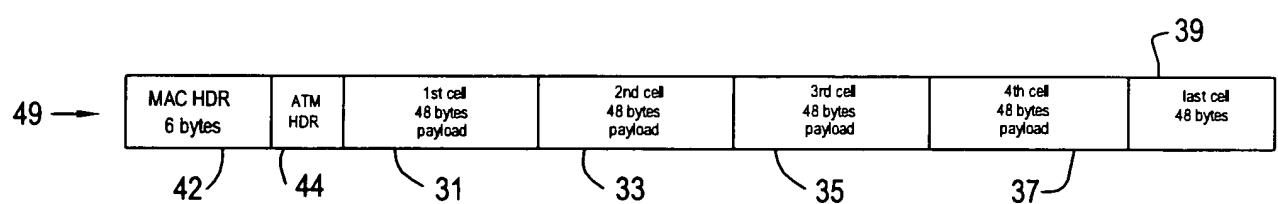


FIG. 7

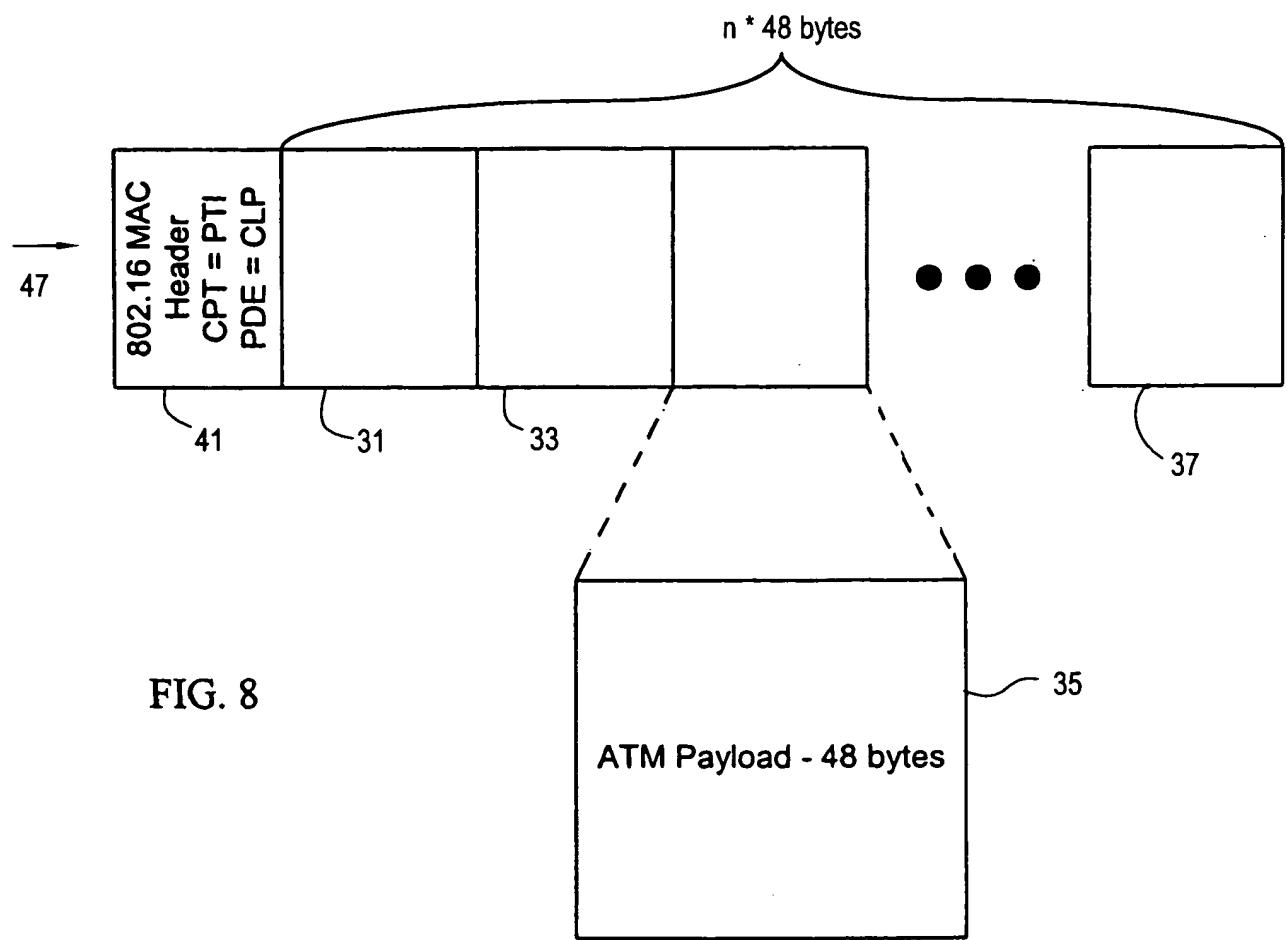


FIG. 9

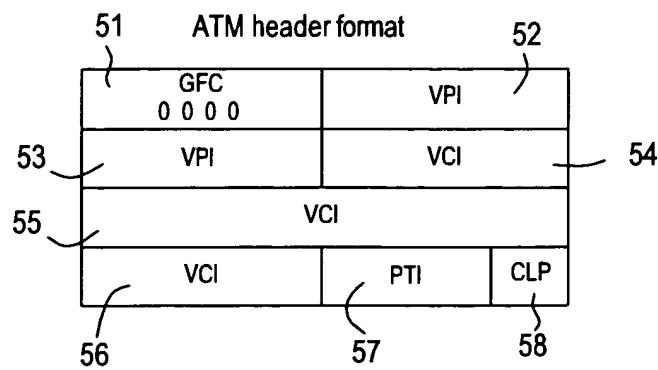


FIG. 10

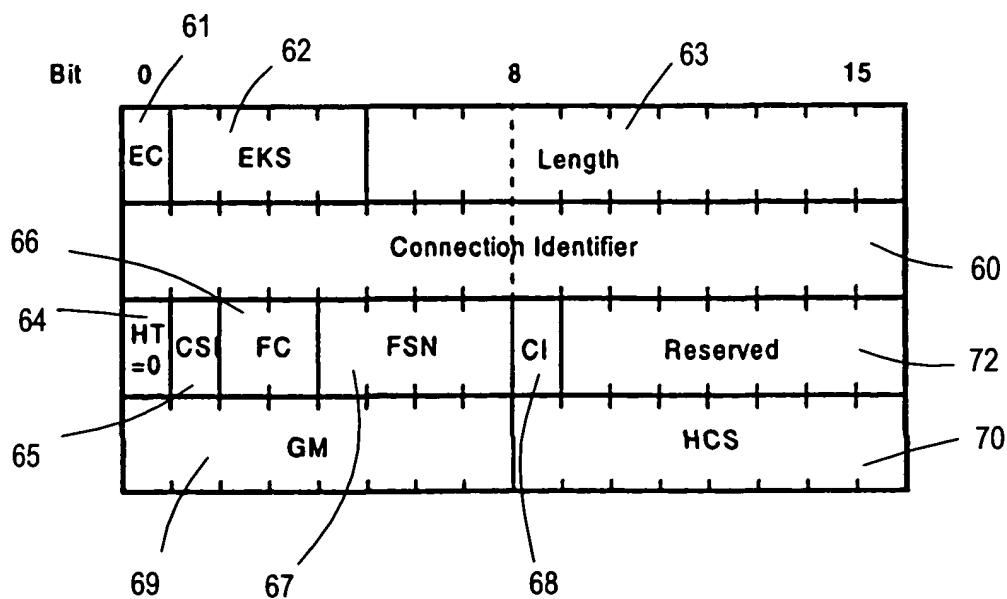
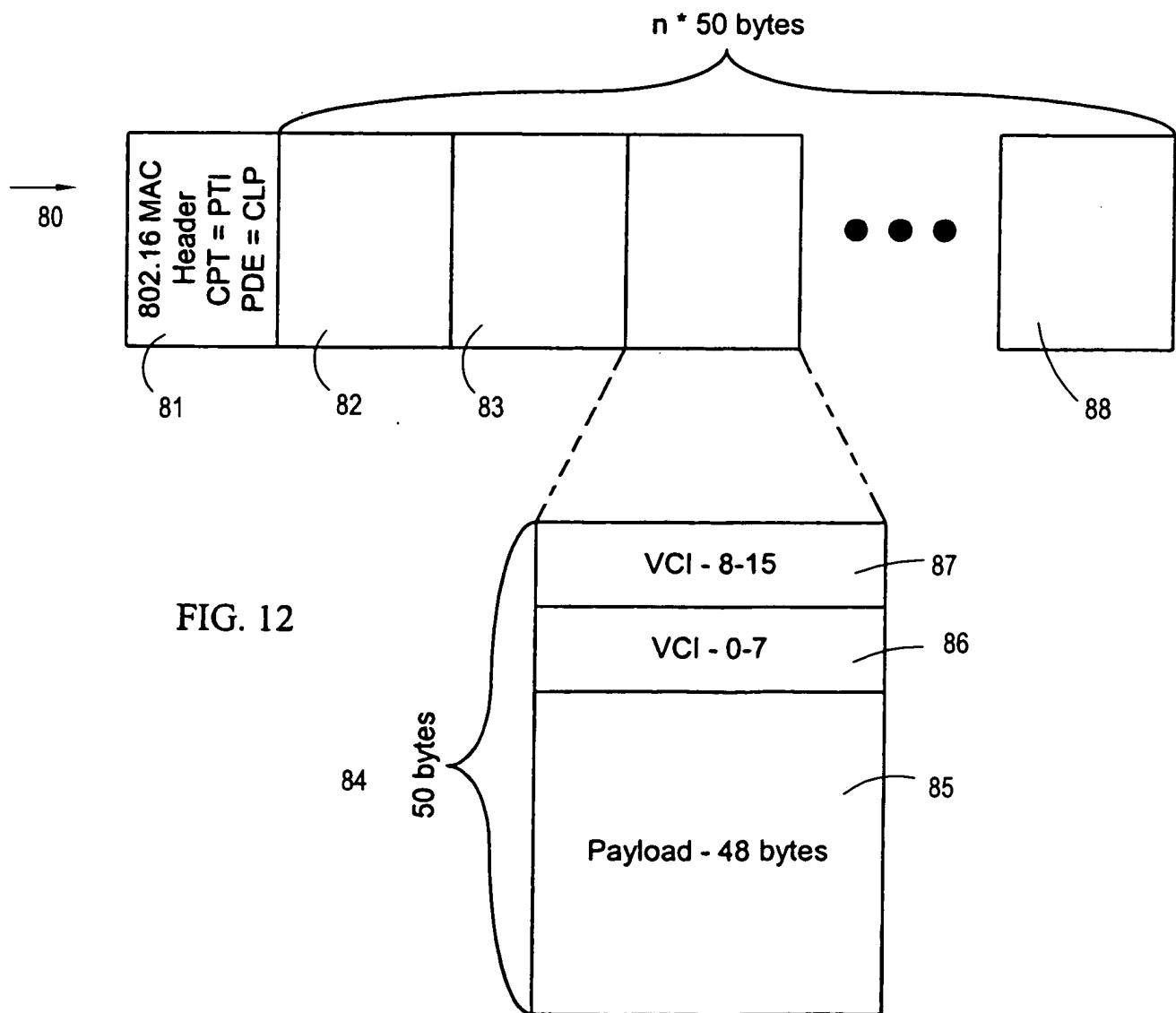
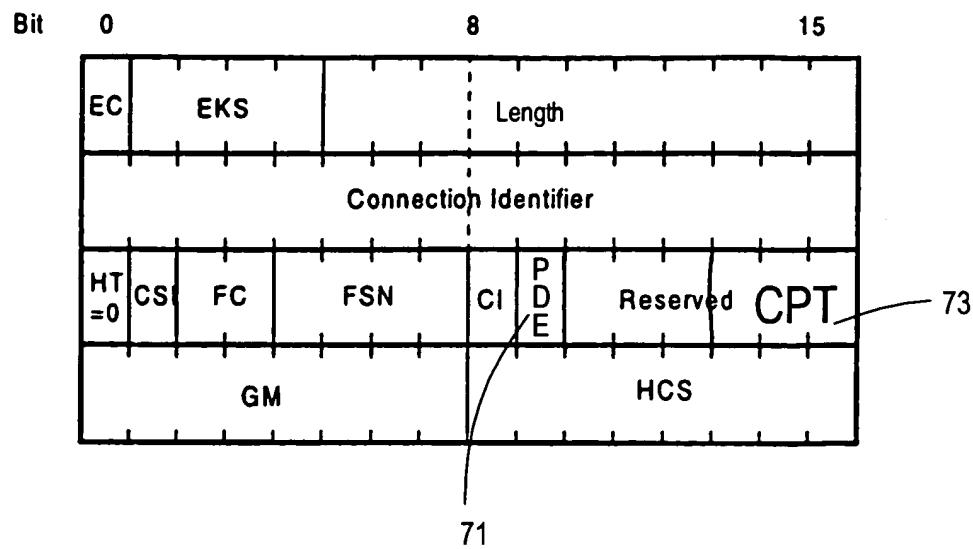


FIG. 11



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MAC header format

EH 1	PC/PM	CIDa
CIDa		CIDb
CIDb		
CIDb	EPT	PDE
Length		

ATM header format

GFC 0 0 0 0	VPI	
VPI	VCI	
VCI		
VCI	PTI	CLP

Mapping:

FIG. 13

MAC header	ATM header
EH/PC/PM	GFC (set to zero at the northbound interface between the ATM and MAC)
CIDa	VPI
CIDb	VCI
CPT	PTI
PDE	CLP
Length	N/A no need to map.

